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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP99/06877 12/08/1999

*Yes.*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 10-375320 12/14/1998  
 JAPAN 11-224692 08/06/1999

*Yes.**Sher 2/22/04*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance	JAPAN	27	20	7

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## TITLE

Wiring, thin-film transistor substrate with the wiring, method of manufacture thereof, and liquid crystal display device

FILING FEE RECEIVED 1484	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
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